Power Domain	Pin Num (ball location)	Driving	Pull Type (Bold is reset initial)
DVDD18_IORB	AB5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AB4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG1	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AC3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AF2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AE2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AE1	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AD4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	W4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	Y4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	N26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	P26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	W23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AA25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	W25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	W26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AB26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AB23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	W24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	Y24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AB24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	Y26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AA26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AA23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	Y23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AA24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AC26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AC25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_MSDC1; DVDD28_MSDC1	AC24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_MSDC1; DVDD28_MSDC1	AD27	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC1; DVDD28_MSDC1	AD26	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC1; DVDD28_MSDC1	AD24	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC1; DVDD28_MSDC1	AC23	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC1; DVDD28_MSDC1	AE26	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_SIM2; DVDD28_SIM2	AE24	1.8v driving current : 3.848mA -15.41mA 3.3v driving current : 3.684mA -20.88mA	PU/**PD**
DVDD18_SIM2; DVDD28_SIM2	AE25	1.8v driving current : 3.848mA -15.41mA 3.3v driving current : 3.684mA -20.88mA	PU/**PD**

DVDD18_SIM2; DVDD28_SIM2	AD23	1.8v driving current : 3.848mA -15.41mA 3.3v driving current : 3.684mA -20.88mA	PU/**PD**
DVDD18_SIM1; DVDD28_SIM1	AE23	1.8v driving current : 3.848mA -15.41mA 3.3v driving current : 3.684mA -20.88mA	PU/**PD**
DVDD18_SIM1; DVDD28_SIM1	AE22	1.8v driving current : 3.848mA -15.41mA 3.3v driving current : 3.684mA -20.88mA	PU/**PD**
DVDD18_SIM1; DVDD28_SIM1	AF24	1.8v driving current : 3.848mA -15.41mA 3.3v driving current : 3.684mA -20.88mA	PU/**PD**
DVDD18_IOBL	AF23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AF26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AH23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AH27	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AH24	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOBL	AH25	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOBL	AF22	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOBL	AG22	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOBL	AJ25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AJ24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AH26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AJ26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AH22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AJ22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG20	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AE20	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AF20	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG7	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH7	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AJ8	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AJ7	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AE6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AD6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AF6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AJ5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AD5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AE5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AF5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH5	2/4/6/8/10/12/14/16mA	PU/**PD**

DVDD18_IORB	AC6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AJ4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AF4	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AC5	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AB6	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AE4	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AJ2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AG3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AF3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	W5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AA6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AH1	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AA5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AC4	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AE3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AD2	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AD1	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	W6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	Y5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AA4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AC2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AA3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AA2	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AB2	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	AB1	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	Y2	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORB	W2	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IORT	L5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	K4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	K5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	L3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	L4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	K3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	J4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	J5	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	H4	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	H5	2/4/6/8/10/12/14/16mA	PU/**PD**

DVDD18_IORT	J6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	J7	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	H6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	K6	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORT	H3	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_MSDC0	C27	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	B26	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	D24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_MSDC0	A26	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	C26	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	G23	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	B27	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	C25	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	D26	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	E25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_MSDC0	E24	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_MSDC0	D25	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOLT	K26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	M23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	M24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	L26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	J22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	N27	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	M25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	M26	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	N24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	L23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	P24	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	N23	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOLT	P25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	P23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	R23	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOLT	R24	2/4/6/8/10/12/14/16mA	**PU**/PD
DVDD18_IOLT	R25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	L25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	M22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	L22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	N25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	P22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	T23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	R22	2/4/6/8/10/12/14/16mA	PU/**PD**

DVDD18_IOLT	N22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	T22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLT	K22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	W22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AB25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	Y22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	AA22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	Y25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOLM	V22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AE21	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AF21	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AD25	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG23	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AD21	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG21	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AD20	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AG19	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AD22	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IOBL	AD19	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	AA7	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	Y7	2/4/6/8/10/12/14/16mA	PU/**PD**
DVDD18_IORB	Y6	2/4/6/8/10/12/14/16mA	PU/**PD**

IO Reset default mode	Pin Name	GPIO reset default mode	EINT	Aux Func.0
I	**PAD_EINT0**	0	EINT0	**GPIO0**
I	**PAD_EINT1**	0	EINT1	**GPIO1**
I	**PAD_EINT2**	0	EINT2	**GPIO2**
I	**PAD_EINT3**	0	EINT3	**GPIO3**
I	**PAD_EINT4**	0	EINT4	**GPIO4**
I	**PAD_EINT5**	0	EINT5	**GPIO5**
I	**PAD_EINT6**	0	EINT6	**GPIO6**
I	**PAD_EINT7**	0	EINT7	**GPIO7**
I	**PAD_EINT8**	0	EINT8	**GPIO8**
I	**PAD_EINT9**	0	EINT9	**GPIO9**
I	**PAD_EINT10**	0	EINT10	**GPIO10**
I	**PAD_SCL6**	0	EINT11	**GPIO11**
I	**PAD_SDA6**	0	EINT12	**GPIO12**
I	**PAD_DPI_D0**	0	EINT13	**GPIO13**
I	**PAD_DPI_D1**	0	EINT14	**GPIO14**
I	**PAD_DPI_D2**	0	EINT15	**GPIO15**
I	**PAD_DPI_D3**	0	EINT16	**GPIO16**
I	**PAD_DPI_D4**	0	EINT17	**GPIO17**
I	**PAD_DPI_D5**	0	EINT18	**GPIO18**
I	**PAD_DPI_D6**	0	EINT19	**GPIO19**
I	**PAD_DPI_D7**	0	EINT20	**GPIO20**
I	**PAD_DPI_D8**	0	EINT21	**GPIO21**
I	**PAD_DPI_D9**	0	EINT22	**GPIO22**
I	**PAD_DPI_D10**	0	EINT23	**GPIO23**
I	**PAD_DPI_D11**	0	EINT24	**GPIO24**
I	**PAD_DPI_HSYNC**	0	EINT25	**GPIO25**
I	**PAD_DPI_VSYNC**	0	EINT26	**GPIO26**
I	**PAD_DPI_DE**	0	EINT27	**GPIO27**
I	**PAD_DPI_CK**	0	EINT28	**GPIO28**
OL	**PAD_MSDC1_CLK**	1	EINT29	GPIO29
I	**PAD_MSDC1_DAT3**	1	EINT30	GPIO30
I	**PAD_MSDC1_CMD**	1	EINT31	GPIO31
I	**PAD_MSDC1_DAT0**	1	EINT32	GPIO32
I	**PAD_MSDC1_DAT2**	1	EINT33	GPIO33
I	**PAD_MSDC1_DAT1**	1	EINT34	GPIO34
I	**PAD_SIM2_SIO**	0	EINT35	**GPIO35**
I	**PAD_SIM2_SRST**	0	EINT36	**GPIO36**

I	**PAD_SIM2_SCLK**	0	EINT37	**GPIO37**
I	**PAD_SIM1_SCLK**	0	EINT38	**GPIO38**
I	**PAD_SIM1_SRST**	0	EINT39	**GPIO39**
I	**PAD_SIM1_SIO**	0	EINT40	**GPIO40**
I	**PAD_IDDIG**	0	EINT41	**GPIO41**
I	**PAD_DRVBUS**	0	EINT42	**GPIO42**
I	**PAD_DISP_PWM**	0	EINT43	**GPIO43**
I	**PAD_DSI_TE**	0	EINT44	**GPIO44**
I	**PAD_LCM_RST**	0	EINT45	**GPIO45**
I	**PAD_INT_SIM2**	0	EINT46	**GPIO46**
I	**PAD_INT_SIM1**	0	EINT47	**GPIO47**
I	**PAD_SCL5**	1	EINT48	GPIO48
I	**PAD_SDA5**	1	EINT49	GPIO49
I	**PAD_SCL3**	1	EINT50	GPIO50
I	**PAD_SDA3**	1	EINT51	GPIO51
OL	**PAD_BPI_ANT2**	1	EINT52	GPIO52
OL	**PAD_BPI_ANT0**	1	EINT53	GPIO53
OL	**PAD_BPI_OLAT1**	1	EINT54	GPIO54
OL	**PAD_BPI_BUS8**	1	EINT55	GPIO55
OL	**PAD_BPI_BUS9**	1	EINT56	GPIO56
OL	**PAD_BPI_BUS10**	1	EINT57	GPIO57
I	**PAD_RFIC0_BSI_D2**	1	EINT58	GPIO58
I	**PAD_RFIC0_BSI_D1**	1	EINT59	GPIO59
I	**PAD_RFIC0_BSI_D0**	1	EINT60	GPIO60
I	**PAD_MISC_BSI_DO_1**	1	EINT61	GPIO61
OL	**PAD_MISC_BSI_CK_1**	1	EINT62	GPIO62
I	**PAD_MISC_BSI_DO_0**	1	EINT63	GPIO63
OL	**PAD_MISC_BSI_CK_0**	1	EINT64	GPIO64
I	**PAD_MISC_BSI_DO_3**	1	EINT65	GPIO65
OL	**PAD_MISC_BSI_CK_3**	1	EINT66	GPIO66
I	**PAD_MISC_BSI_DO_2**	1	EINT67	GPIO67
OL	**PAD_MISC_BSI_CK_2**	1	EINT68	GPIO68
OL	**PAD_BPI_BUS7**	1	EINT69	GPIO69
OL	**PAD_BPI_BUS6**	1	EINT70	GPIO70
OL	**PAD_BPI_BUS5**	1	EINT71	GPIO71
OL	**PAD_BPI_BUS4**	1	EINT72	GPIO72
OL	**PAD_BPI_BUS3**	1	EINT73	GPIO73
OL	**PAD_BPI_BUS2**	1	EINT74	GPIO74
OL	**PAD_BPI_BUS1**	1	EINT75	GPIO75

OL	**PAD_BPI_BUS0**	1	EINT76	GPIO76
OL	**PAD_BPI_ANT1**	1	EINT77	GPIO77
OL	**PAD_BPI_OLAT0**	1	EINT78	GPIO78
OL	**PAD_BPI_PA_VM1**	1	EINT79	GPIO79
OL	**PAD_BPI_PA_VM0**	1	EINT80	GPIO80
I	**PAD_SDA1**	1	EINT81	GPIO81
I	**PAD_SDA0**	1	EINT82	GPIO82
I	**PAD_SCL0**	1	EINT83	GPIO83
I	**PAD_SCL1**	1	EINT84	GPIO84
I	**PAD_SPI_MI**	0	EINT85	**GPIO85**
I	**PAD_SPI_CSB**	0	EINT86	**GPIO86**
I	**PAD_SPI_MO**	0	EINT87	**GPIO87**
I	**PAD_SPI_CLK**	0	EINT88	**GPIO88**
I	**PAD_SRCLKENAI**	0	EINT89	**GPIO89**
I	**PAD_PWM_A**	0	EINT90	**GPIO90**
I	**PAD_KPROW1**	0	EINT91	**GPIO91**
OL	**PAD_KPROW0**	1	EINT92	GPIO92
I	**PAD_KPCOL0**	1	EINT93	GPIO93
I	**PAD_KPCOL1**	0	EINT94	**GPIO94**
I	**PAD_URXD0**	1	EINT95	GPIO95
OH	**PAD_UTXD0**	1	EINT96	GPIO96
I	**PAD_CAM_PDN0**	0	EINT97	**GPIO97**
I	**PAD_CAM_PDN1**	0	EINT98	**GPIO98**
I	**PAD_CAM_CLK0**	0	EINT99	**GPIO99**
I	**PAD_CAM_CLK1**	0	EINT100	**GPIO100**
I	**PAD_CAM_RST0**	0	EINT101	**GPIO101**
I	**PAD_CAM_RST1**	0	EINT102	**GPIO102**
I	**PAD_SCL2**	1	EINT103	GPIO103
I	**PAD_SDA2**	1	EINT104	GPIO104
I	**PAD_SCL4**	1	EINT105	GPIO105
I	**PAD_SDA4**	1	EINT106	GPIO106
I	**PAD_CAM_PDN2**	0	EINT107	**GPIO107**
I	**PAD_CAM_CLK2**	0	EINT108	**GPIO108**
I	**PAD_CAM_RST2**	0	EINT109	**GPIO109**
I	**PAD_CAM_PDN3**	0	EINT110	**GPIO110**
I	**PAD_CAM_CLK3**	0	EINT111	**GPIO111**
I	**PAD_CAM_RST3**	0	EINT112	**GPIO112**
I	**PAD_CONN_TOP_CLK**	0	EINT113	GPIO113
I	**PAD_CONN_TOP_DATA**	0	EINT114	GPIO114
I	**PAD_CONN_BT_CLK**	0	EINT115	GPIO115
I	**PAD_CONN_BT_DATA**	0	EINT116	GPIO116

I	**PAD_CONN_WF_CTRL0**	0	EINT117	GPIO117
I	**PAD_CONN_WF_CTRL1**	0	EINT118	GPIO118
I	**PAD_CONN_WF_CTRL2**	0	EINT119	GPIO119
I	**PAD_CONN_WB_PTA**	0	EINT120	GPIO120
I	**PAD_CONN_HRST_B**	0	EINT121	GPIO121
I	**PAD_MSDC0_CMD**	1	EINT122	GPIO122
I	**PAD_MSDC0_DAT0**	1	EINT123	GPIO123
OL	**PAD_MSDC0_CLK**	1	EINT124	GPIO124
I	**PAD_MSDC0_DAT2**	1	EINT125	GPIO125
I	**PAD_MSDC0_DAT4**	1	EINT126	GPIO126
I	**PAD_MSDC0_DAT6**	1	EINT127	GPIO127
I	**PAD_MSDC0_DAT1**	1	EINT128	GPIO128
I	**PAD_MSDC0_DAT5**	1	EINT129	GPIO129
I	**PAD_MSDC0_DAT7**	1	EINT130	GPIO130
I	**PAD_MSDC0_DSL**	1	EINT131	GPIO131
I	**PAD_MSDC0_DAT3**	1	EINT132	GPIO132
OH	**PAD_MSDC0_RSTB**	1	EINT133	GPIO133
I	**PAD_RTC32K_CK**	1	EINT134	GPIO134
OL	**PAD_WATCHDOG**	1	EINT135	GPIO135
I	**PAD_AUD_CLK_MOSI**	0	EINT136	GPIO136
I	**PAD_AUD_SYNC_MOSI**	0	EINT137	GPIO137
I	**PAD_AUD_DAT_MOSI0**	0	EINT138	GPIO138
I	**PAD_AUD_DAT_MOSI1**	0	EINT139	GPIO139
I	**PAD_AUD_CLK_MISO**	0	EINT140	GPIO140
I	**PAD_AUD_SYNC_MISO**	0	EINT141	GPIO141
I	**PAD_AUD_DAT_MISO0**	0	EINT142	GPIO142
I	**PAD_AUD_DAT_MISO1**	0	EINT143	GPIO143
I	**PAD_PWRAP_SPI0_MI**	1	EINT144	GPIO144
OH	**PAD_PWRAP_SPI0_CSN**	1	EINT145	GPIO145
I	**PAD_PWRAP_SPI0_MO**	1	EINT146	GPIO146
OL	**PAD_PWRAP_SPI0_CK**	1	EINT147	GPIO147
OH	**PAD_SRCLKENA0**	1	EINT148	GPIO148
OL	**PAD_SRCLKENA1**	1	EINT149	GPIO149
I	**PAD_PERIPHERAL_EN0**	0	EINT150	**GPIO150**
I	**PAD_PERIPHERAL_EN1**	0	EINT151	**GPIO151**
I	**PAD_PERIPHERAL_EN2**	0	EINT152	**GPIO152**
I	**PAD_PERIPHERAL_EN3**	0	EINT153	**GPIO153**
I	**PAD_SCP_VREQ_VAO**	0	EINT154	**GPIO154**
I	**PAD_ANT_SEL0**	0	EINT155	**GPIO155**
I	**PAD_ANT_SEL1**	0	EINT156	**GPIO156**
I	**PAD_ANT_SEL2**	0	EINT157	**GPIO157**

I	**PAD_PERIPHERAL_EN6**	0	EINT158	**GPIO158**
I	**PAD_PERIPHERAL_EN7**	0	EINT159	**GPIO159**
I	**PAD_PERIPHERAL_EN8**	0	EINT160	**GPIO160**
I	**PAD_SPI1_MI**	0	EINT161	**GPIO161**
I	**PAD_SPI1_CSB**	0	EINT162	**GPIO162**
I	**PAD_SPI1_MO**	0	EINT163	**GPIO163**
I	**PAD_SPI1_CLK**	0	EINT164	**GPIO164**
I	**PAD_PERIPHERAL_EN4**	0	EINT165	**GPIO165**
I	**PAD_PERIPHERAL_EN9**	0	EINT166	**GPIO166**
OL	**PAD_RFIC0_BSI_EN**	1	EINT167	GPIO167
OL	**PAD_RFIC0_BSI_CK**	1	EINT168	GPIO168
I	**PAD_PERIPHERAL_EN5**	0	EINT169	**GPIO169**
I	**PAD_I2S1_BCK**	0	EINT170	**GPIO170**
I	**PAD_I2S1_LRCK**	0	EINT176	**GPIO171**
I	**PAD_I2S1_DO**	0	EINT177	**GPIO172**
I	**PAD_I2S1_MCK**	0	EINT178	**GPIO173**
I	**PAD_I2S2_DI**	0	EINT179	**GPIO174**
I	**PAD_PERIPHERAL_EN12**	0	EINT180	**GPIO175**
I	**PAD_PERIPHERAL_EN13**	0	EINT181	**GPIO176**
I	**PAD_PERIPHERAL_EN14**	0	EINT182	**GPIO177**
I	**PAD_PERIPHERAL_EN10**	0	EINT183	**GPIO178**
I	**PAD_PERIPHERAL_EN11**	0	EINT184	**GPIO179**

Aux Func.1	Aux Func.2	Aux Func.3
O:MRG_SYNC	O:PCM0_SYNC	B0:TP_GPIO0_AO
O*MRG_CLK	O*PCM0_CLK	B0:TP_GPIO1_AO
O:MRG_DO	O:PCM0_DO	B0:TP_GPIO2_AO
I0:MRG_DI	I0:PCM0_DI	B0:TP_GPIO3_AO
O:PWM_B	O*I2S0_MCK	O:SSPM_UTXD_AO
O:PWM_C	B0*I2S0_BCK	I1:SSPM_URXD_AO
O:PWM_A	B0:I2S0_LRCK	I1:IDDIG
I0*SPI1_B_MI	I0:I2S0_DI	O:USB_DRVVBUS
O*SPI1_B_CSB	O:ANT_SEL3	B1:SCL7
O*SPI1_B_MO	O:ANT_SEL4	O*CMMCLK2
O*SPI1_B_CLK	O:ANT_SEL5	O*CMMCLK3
I1:TP_URXD1_AO	I1:IDDIG	B1:SCL6
O:TP_UTXD1_AO	O:USB_DRVVBUS	B1:SDA6
B0:DBPI_D0	I0*SPI5_MI	O:PCM0_SYNC
B0:DBPI_D1	O*SPI5_CSB	O*PCM0_CLK
B0:DBPI_D2	O*SPI5_MO	O:PCM0_DO
B0:DBPI_D3	O*SPI5_CLK	I0:PCM0_DI
B0:DBPI_D4	I0*SPI4_MI	I0:CONN_MCU_TRST_B
B0:DBPI_D5	O*SPI4_CSB	I1:CONN_MCU_DBGI_N
B0:DBPI_D6	O*SPI4_MO	O:CONN_MCU_TDO
B0:DBPI_D7	O*SPI4_CLK	O:CONN_MCU_DBGACK_N
B0:DBPI_D8	I0*SPI3_MI	I1:CONN_MCU_TMS
B0:DBPI_D9	O*SPI3_CSB	I0*CONN_MCU_TCK
B0:DBPI_D10	O*SPI3_MO	I0:CONN_MCU_TDI
B0:DBPI_D11	O*SPI3_CLK	I0:SRCLKENAI0
O*DBPI_HSYNC	O:ANT_SEL0	B1:SCL6
O*DBPI_VSYNC	O:ANT_SEL1	B1:SDA6
O*DBPI_DE	O:ANT_SEL2	B1:SCL7
O*DBPI_CK	I0:DVFSRC_EXT_REQ	B1:SDA7
O*MSDC1_CLK	I0*IO_JTAG_TCK	I0*UDI_TCK
B1*MSDC1_DAT3	B1:DAP_MD32_SWD	B1:CONN_MCU_AICE_TMSC
B1*MSDC1_CMD	B1:IO_JTAG_TMS	I0:UDI_TMS
B1*MSDC1_DAT0	I1:IO_JTAG_TDI	I0:UDI_TDI
B1*MSDC1_DAT2	I1:IO_JTAG_TRSTN	I0:UDI_NTRST
B1*MSDC1_DAT1	O:IO_JTAG_TDO	O:UDI_TDO
B1*MD1_SIM2_SIO	O:CCU_JTAG_TDO	B1*MD1_SIM1_SIO
O*MD1_SIM2_SRST	I1:CCU_JTAG_TMS	O*MD1_SIM1_SRST

O*MD1_SIM2_SCLK	I1:CCU_JTAG_TDI	O*MD1_SIM1_SCLK
O*MD1_SIM1_SCLK		O*MD1_SIM2_SCLK
O*MD1_SIM1_SRST	I1*CCU_JTAG_TCK	O*MD1_SIM2_SRST
B1*MD1_SIM1_SIO	I1:CCU_JTAG_TRST	B1*MD1_SIM2_SIO
I1:IDDIG	I1:URXD1	I1:UCTS0
O:USB_DRVVBUS	O:UTXD1	O:URTS0
O:DISP_PWM		
I0:DSI_TE		
O:LCM_RST		
I0:MD_INT2_C2K_UIM1_HOT_PLUG	I1:URXD1	I1:UCTS1
I0:MD_INT1_C2K_UIM0_HOT_PLUG	O:UTXD1	O:URTS1
B1:SCL5		
B1:SDA5		
B1:SCL3		
B1:SDA3		
O*BPI_ANT2		
O*BPI_ANT0		
O*BPI_OLAT1		
O*BPI_BUS8		
O*BPI_BUS9	O: SCL_6306	
O*BPI_BUS10	B1: SDA_6306	
B0*RFIC0_BSI_D2	O*SPM_BSI_D2	O:PWM_B
B0*RFIC0_BSI_D1	O*SPM_BSI_D1	
B0*RFIC0_BSI_D0	O*SPM_BSI_D0	
B0*MIPI1_SDATA		
O*MIPI1_SCLK		
B0*MIPI0_SDATA		
O*MIPI0_SCLK		
B0*MIPI3_SDATA	O*BPI_OLAT2	
O*MIPI3_SCLK	O*BPI_OLAT3	
B0*MIPI2_SDATA		
O*MIPI2_SCLK		
O*BPI_BUS7		
O*BPI_BUS6		
O*BPI_BUS5		
O*BPI_BUS4		
O*BPI_BUS3		
O*BPI_BUS2		
O*BPI_BUS1		

O*BPI_BUS0		
O*BPI_ANT1		
O*BPI_OLAT0		
O:BPI_PA_VM1	B0*MIPI4_SDATA	
O:BPI_PA_VM0	O*MIPI4_SCLK	
B1:SDA1		
B1:SDA0		
B1:SCL0		
B1:SCL1		
I0*SPI0_MI	I0*SCP_SPI0_MI	O*CLKM3
O*SPI0_CSB	O*SCP_SPI0_CS	O*CLKM0
O*SPI0_MO	O*SCP_SPI0_MO	B1:SDA1
O*SPI0_CLK	O*SCP_SPI0_CK	B1:SCL1
I0:SRCLKENAI0	O:PWM_C	O*I2S5_BCK
O:PWM_A	O*CMMCLK2	O:I2S5_LRCK
B1:KPROW1	O:PWM_B	O:I2S5_DO
B1:KPROW0		
B1:KPCOL0		
B1:KPCOL1	I0:I2S2_DI2	O*I2S5_MCK
I1:URXD0	O:UTXD0	I1:MD_URXD0
O:UTXD0	I1:URXD0	O:MD_UTXD0
I1:UCTS0	O*I2S2_MCK	I1:IDDIG
O:URTS0	O*I2S2_BCK	O:USB_DRVVBUS
O*CMMCLK0		
O*CMMCLK1	O:PWM_C	I0:MD_INT1_C2K_UIM0_HOT_PLUG
O*CLKM2	O:I2S2_LRCK	O*CMVREF1
O*CLKM1	I0:I2S2_DI	I0:DVFSRC_EXT_REQ
B1:SCL2		
B1:SDA2		
B1:SCL4		
B1:SDA4		
O*DMIC_CLK	O:ANT_SEL0	O*CLKM0
O*CMMCLK2	O:ANT_SEL1	O*CLKM1
I0*DMIC_DAT	O:ANT_SEL2	O*CLKM2
B1:SCL7	O:ANT_SEL0	I1:TP_URXD1_AO
O*CMMCLK3	O:ANT_SEL1	I0:SRCLKENAI0
B1:SDA7	O:ANT_SEL2	O:TP_UTXD1_AO
O:CONN_TOP_CLK		B1:SCL6
B0:CONN_TOP_DATA		B1:SDA6
B0:CONN_BT_CLK	O:UTXD1	O:PTA_TXD
B0:CONN_BT_DATA	I0:IPU_JTAG_TRST	

B0:CONN_WF_HB0	O:IPU_JTAG_TDO	
B0:CONN_WF_HB1	I0:IPU_JTAG_TDI	
B0:CONN_WF_HB2	I0*IPU_JTAG_TCK	
B0:CONN_WB_PTA	I0:IPU_JTAG_TMS	
O:CONN_HRST_B	I1:URXD1	I1:PTA_RXD
B1*MSDC0_CMD	**I1:PWRMCU_URXD2_AO**	O:ANT_SEL1
B1*MSDC0_DAT0		O:ANT_SEL0
O*MSDC0_CLK		
B1*MSDC0_DAT2		O*MRG_CLK
B1*MSDC0_DAT4		O:ANT_SEL5
B1*MSDC0_DAT6		O:ANT_SEL4
B1*MSDC0_DAT1		O:ANT_SEL2
B1*MSDC0_DAT5		O:ANT_SEL3
B1*MSDC0_DAT7		O:MRG_DO
I0*MSDC0_DSL		O:MRG_SYNC
B1*MSDC0_DAT3		I0:MRG_DI
O*MSDC0_RSTB		O:AGPS_SYNC
I0*RTC32K_CK		
O:WATCHDOG		
O*AUD_CLK_MOSI	I0*AUD_CLK_MISO	O*I2S1_MCK
O*AUD_SYNC_MOSI	I0*AUD_SYNC_MISO	O*I2S1_BCK
O*AUD_DAT_MOSI0	I0*AUD_DAT_MISO0	O:I2S1_LRCK
O*AUD_DAT_MOSI1	I0*AUD_DAT_MISO1	O:I2S1_DO
I0*AUD_CLK_MISO	O*AUD_CLK_MOSI	O*I2S0_MCK
I0*AUD_SYNC_MISO	O*AUD_SYNC_MOSI	O*I2S0_BCK
I0*AUD_DAT_MISO0	O*AUD_DAT_MOSI0	O:I2S0_LRCK
I0*AUD_DAT_MISO1	O*AUD_DAT_MOSI1	I0:I2S0_DI
B0*PWRAP_SPI0_MI	B0*PWRAP_SPI0_MO	
O*PWRAP_SPI0_CSN		
B0*PWRAP_SPI0_MO	B0*PWRAP_SPI0_MI	
O*PWRAP_SPI0_CK		
O:SRCLKENA0		
O:SRCLKENA1		
O:PWM_A	O:CMFLASH	O*CLKM0
O:PWM_B	O*CMVREF0	O*CLKM1
O:PWM_C	O:CMFLASH	O*CLKM2
O:PWM_A	O*CMVREF0	O*CLKM3
O:SCP_VREQ_VAO	I0:DVFSRC_EXT_REQ	
O:ANT_SEL0	I0:DVFSRC_EXT_REQ	O*CMVREF1
O:ANT_SEL1	I0:SRCLKENAI0	B1:SCL6
O:ANT_SEL2	I0:SRCLKENAI1	B1:SDA6

O:ANT_SEL3		
O:ANT_SEL4		
O:ANT_SEL5		
I0*SPI1_A_MI	I0*SCP_SPI1_MI	I1:IDDIG
O*SPI1_A_CSB	O*SCP_SPI1_CS	O:USB_DRVVBUS
O*SPI1_A_MO	O*SCP_SPI1_MO	B1:SDA1
O*SPI1_A_CLK	O*SCP_SPI1_CK	B1:SCL1
O:PWM_B	O*CMMCLK2	O:SCP_VREQ_VAO
O:ANT_SEL6		
O*RFIC0_BSI_EN	O*SPM_BSI_EN	
O*RFIC0_BSI_CK	O*SPM_BSI_CK	
O:PWM_C	O*CMMCLK3	O*CMVREF1
O*I2S1_BCK	O*I2S3_BCK	B1:SCL7
O:I2S1_LRCK	O:I2S3_LRCK	B1:SDA7
O:I2S1_DO	O:I2S3_DO	B1:SCL8
O*I2S1_MCK	O*I2S3_MCK	B1:SDA8
I0:I2S2_DI	I0:I2S0_DI	I0:DVFSRC_EXT_REQ
O:ANT_SEL7		

Aux Func.4	Aux Func.5	Aux Func.6
I0:SRCLKENAI0	O*SCP_SPI2_CS	O*I2S3_MCK
O*CLKM3	O*SCP_SPI2_MO	O*I2S3_BCK
B1:SCL6	O*SCP_SPI2_CK	O:I2S3_LRCK
B1:SDA6	O*TDM_MCK	O:I2S3_DO
I1:MD_URXD1	O*TDM_BCK	B0:TP_GPIO4_AO
O:MD_UTXD1	O*TDM_LRCK	B0:TP_GPIO5_AO
I1:MD_URXD0	O*TDM_DATA0	B0:TP_GPIO6_AO
O:MD_UTXD0	O*TDM_DATA1	B0:TP_GPIO7_AO
I0:CONN_MCU_TRST_B	O*TDM_DATA2	I0:MD_INT0
O:CONN_MCU_DBGACK_N	I0:SSPM_JTAG_TRSTN	I1:IO_JTAG_TRSTN
I1:CONN_MCU_DBGI_N	O*TDM_DATA3	I0:EXT_FRAME_SYNC
I1:UCTS1	I1:UCTS0	I0:SRCLKENAI1
O:URTS1	O:URTS0	I0:I2S2_DI2
I1:MD_URXD0	O:ANT_SEL3	O*I2S0_MCK
O:MD_UTXD0	O:ANT_SEL4	B0*I2S0_BCK
I1:MD_URXD1	O:ANT_SEL5	B0:I2S0_LRCK
O:MD_UTXD1	O:ANT_SEL6	I0:I2S0_DI
I0:MD_INT0	O:ANT_SEL7	O*I2S3_MCK
I0:MD_INT0	O:SCP_VREQ_VAO	O*I2S3_BCK
I0:MD_INT2_C2K_UIM1_HOT_PLUG	I1:URXD1	O:I2S3_LRCK
I0:MD_INT1_C2K_UIM0_HOT_PLUG	O:UTXD1	O:I2S3_DO
B1:DAP_MD32_SWD	B1:CONN_MCU_AICE_TMSC	O*I2S2_MCK
I0*DAP_MD32_SWCK	I0*CONN_MCU_AICE_TCKC	O*I2S2_BCK
I1:UCTS1	I0:EXT_FRAME_SYNC	O:I2S2_LRCK
O:URTS1	I0*IO_JTAG_TCK	I0:I2S2_DI
B1:KPCOL2	B1:IO_JTAG_TMS	O*I2S1_MCK
B1:KPROW2	I1:IO_JTAG_TDI	O*I2S1_BCK
O*DMIC_CLK	O:IO_JTAG_TDO	O:I2S1_LRCK
I0*DMIC_DAT	I1:IO_JTAG_TRSTN	O:I2S1_DO
I0*CONN_DSP_JCK	I1*SSPM_JTAG_TCK	B0*PCM1_CLK
O:CONN_DSP_JINTP	I0:SSPM_JTAG_TRSTN	I0:PCM1_DI
I1:CONN_DSP_JMS	I1:SSPM_JTAG_TMS	B0:PCM1_SYNC
I0:CONN_DSP_JDI	I1:SSPM_JTAG_TDI	O:PCM1_DO0
I0*DAP_MD32_SWCK	I0*CONN_MCU_AICE_TCKC	O:PCM1_DO2
O:CONN_DSP_JDO	B1:SSPM_JTAG_TDO	O:PCM1_DO1
	O:SCP_JTAG_TDO	I1:CONN_DSP_JMS
B1:CONN_MCU_AICE_TMSC	B1:SCP_JTAG_TMS	O:CONN_DSP_JINTP

	I1:SCP_JTAG_TDI	O:CONN_DSP_JDO
I0*CONN_MCU_AICE_TCKC		
	I1*SCP_JTAG_TCK	I0*CONN_DSP_JCK
	I0:SCP_JTAG_TRSTN	I0:CONN_DSP_JDI
O:SSPM_UTXD_AO	I0:EXT_FRAME_SYNC	O*DMIC_CLK
I1:SSPM_URXD_AO	I0:EXT_FRAME_SYNC	I0*DMIC_DAT
O:CCU_UTXD_AO	I1:TP_UCTS1_AO	I1:IDDIG
I1:CCU_URXD_AO	O:TP_URTS1_AO	O:USB_DRVVBUS

O*I2S1_BCK	O:MFG_DFD_JTAG_TDO	O:DFD_TDO
O:I2S1_LRCK	I0:MFG_DFD_JTAG_TMS	I0:DFD_TMS
O:I2S1_DO	I0:MFG_DFD_JTAG_TDI	I0:DFD_TDI
O*I2S1_MCK	I0*MFG_DFD_JTAG_TCK	I0*DFD_TCK_XI
O:ANT_SEL6	B1:SDA8	O*CMVREF0
O:SCP_VREQ_VAO	B1:SCL8	I1:PTA_RXD
O:ANT_SEL7	O*CMMCLK3	O:PTA_TXD
O*CMMCLK2	I0*SCP_SPI2_MI	I0:SRCLKENAI1
I1:MD_URXD1	I1:SSPM_URXD_AO	I1:CCU_URXD_AO
O:MD_UTXD1	O:SSPM_UTXD_AO	O:CCU_UTXD_AO
O:CONN_MCU_TDO	B1:SSPM_JTAG_TDO	O:IO_JTAG_TDO
I1:CONN_MCU_TMS	I1:SSPM_JTAG_TMS	B1:IO_JTAG_TMS
B1:CONN_MCU_AICE_TMSC		
I0*CONN_MCU_AICE_TCKC		
I0*CONN_MCU_TCK	I1*SSPM_JTAG_TCK	I0*IO_JTAG_TCK
I0:CONN_MCU_TDI	I1:SSPM_JTAG_TDI	I1:IO_JTAG_TDI
B1:SDA7	I0:EXT_FRAME_SYNC	O:PWM_A
B1:SCL8	B1:DAP_MD32_SWD	O:PWM_B
B1:SDA8	I0*DAP_MD32_SWCK	O:PWM_C
O:USB_DRVVBUS	I0:SRCLKENAI1	B1:KPCOL2
O:SCP_VREQ_VAO	I0:MD_INT2_C2K_UIM1_HOT_PLUG	
I1:IDDIG	O:AGPS_SYNC	B1:KPROW2
O:AUXIF_CLK0		I1:TP_UCTS1_AO
O:AUXIF_ST0		O:TP_URTS1_AO
O:AUXIF_CLK1	B1:DAP_MD32_SWD	O:TP_UTXD1_AO
O:AUXIF_ST1	I0*DAP_MD32_SWCK	I1:TP_URXD2_AO

		O:TP_UTXD2_AO
	I1:SSPM_URXD_AO	I1:TP_UCTS2_AO
	O:SSPM_UTXD_AO	O:TP_URTS2_AO
	I1:CCU_URXD_AO	
	O:CCU_UTXD_AO	I1:TP_URXD1_AO
		I1*UFS_MPHY_SCL
		B1*UFS_MPHY_SDA
		B1*UFS_UNIPRO_SDA
		I1*UFS_UNIPRO_SCL
		I1*UFS_UNIPRO_SCL
		B1*UFS_MPHY_SDA
		B1*UFS_UNIPRO_SDA
I0*VOW_DAT_MISO		
I0*VOW_CLK_MISO		I1*UFS_MPHY_SCL
B1:KPCOL2	I1:IDDIG	
B1:KPROW2	O:USB_DRVVBUS	

O:ANT_SEL6	B1:KPCOL2	I1:PTA_RXD
O:ANT_SEL5	B1:KPROW2	O:PTA_TXD
O:ANT_SEL4	O*CMMCLK2	O*DMIC_CLK
O:ANT_SEL3	O*CMMCLK3	I0*DMIC_DAT
		O*TDM_MCK_2nd
O:ANT_SEL7	O:AGPS_SYNC	O*TDM_BCK_2nd
O*I2S5_BCK	I0:EXT_FRAME_SYNC	O*TDM_LRCK_2nd
O*I2S5_LRCK	I1:URXD1	O*TDM_DATA0_2nd
O:I2S5_DO	O:UTXD1	O*TDM_DATA1_2nd
O*I2S5_MCK	I1:UCTS0	O*TDM_DATA2_2nd
I0:I2S2_DI2	O:URTS0	O*TDM_DATA3_2nd

Aux Func.7
O*SPI2_CSB
O*SPI2_MO
O*SPI2_CLK
O:SCP_VREQ_VAO
B1:DAP_MD32_SWD
I0*DAP_MD32_SWCK
O:CMFLASH
I0:DVFSRC_EXT_REQ
I1:JTRSTN_SEL1
B0:DBG_MON_B10
B0:DBG_MON_B11
O*I2S5_MCK
O*I2S5_BCK
B0:DBG_MON_B15
B0:DBG_MON_B16
B0:DBG_MON_B17
B0:DBG_MON_B23
B0:DBG_MON_A1
B0:DBG_MON_A2
B0:DBG_MON_A3
B0:DBG_MON_A19
B0:DBG_MON_B5
B0:DBG_MON_B6
B0:DBG_MON_B7
B0:DBG_MON_B31
B0:DBG_MON_B0
B0:DBG_MON_B1
B0:DBG_MON_B9
B0:DBG_MON_B32
B0:DBG_MON_A6
B0:DBG_MON_A7
B0:DBG_MON_A8
B0:DBG_MON_A9
B0:DBG_MON_A10
B0:DBG_MON_A11
B0:DBG_MON_A28
B0:DBG_MON_A29

B0:DBG_MON_A30
B0:DBG_MON_A20
B0:DBG_MON_A31
B0:DBG_MON_A32
O:I2S5_LRCK
O:I2S5_DO

O:JTDO_SEL1
B1:JTMS_SEL1
I1:JTDI_SEL1
I0*:JTCK_SEL1
B0:DBG_MON_A21
B0:DBG_MON_A22
B0:DBG_MON_B27
I0*SPI2_MI
B0:DBG_MON_B2
B0:DBG_MON_B3
B0:DBG_MON_B4
B0:DBG_MON_B28
B0:DBG_MON_B29
B0:DBG_MON_B8
B0:DBG_MON_B12
B0:DBG_MON_B13
B0:DBG_MON_B14
I1:URXD1
I0:DVFSRC_EXT_REQ
O:UTXD1
B0:DBG_MON_A0

B0:DBG_MON_A4
B0:DBG_MON_A5
B0:DBG_MON_A12
B0:DBG_MON_A13
B0:DBG_MON_A14
B0:DBG_MON_A15
B0:DBG_MON_A16
B0:DBG_MON_A17
B0:DBG_MON_A18
B0:DBG_MON_A23
B0:DBG_MON_A24
B0:DBG_MON_A25
B0:DBG_MON_A26
B0:DBG_MON_A27
B0:DBG_MON_B24
B0:DBG_MON_B25
B0:DBG_MON_B26
B0:DBG_MON_B30
B0:DBG_MON_B20
B0:DBG_MON_B21
B0:DBG_MON_B22
B0:DBG_MON_B18
I1:SCP_JTAG_TDI
I1*SCP_JTAG_TCK
I0:SCP_JTAG_TRSTN

B0:DBG_MON_B19
O:SCP_JTAG_TDO
B1:SCP_JTAG_TMS
O:ANT_SEL3
O:ANT_SEL4
O:ANT_SEL5
O:ANT_SEL6
O:ANT_SEL7

K23	PAD_SYSRSTB
L24	PAD_TESTMODE
A26	PAD_MSDC0_DAT2
B26	PAD_MSDC0_DAT0
B27	PAD_MSDC0_DAT1
C25	PAD_MSDC0_DAT5
C26	PAD_MSDC0_DAT4
C27	PAD_MSDC0_CMD
D24	PAD_MSDC0_CLK
D25	PAD_MSDC0_RSTB
D26	PAD_MSDC0_DAT7
E24	PAD_MSDC0_DAT3
E25	PAD_MSDC0_DSL
G23	PAD_MSDC0_DAT6
H3	PAD_CONN_HRST_B
H4	PAD_CONN_BT_CLK
H5	PAD_CONN_BT_DATA
H6	PAD_CONN_WF_CTRL2
J4	PAD_CONN_TOP_CLK
J5	PAD_CONN_TOP_DATA
J6	PAD_CONN_WF_CTRL0
J7	PAD_CONN_WF_CTRL1
J22	PAD_AUD_DAT_MOSI0
K3	PAD_CAM_RST3
K4	PAD_CAM_CLK2
K5	PAD_CAM_RST2
K6	PAD_CONN_WB_PTA
K22	PAD_PERIPHERAL_EN8
K26	PAD_RTC32K_CK
L3	PAD_CAM_PDN3
L4	PAD_CAM_CLK3
L5	PAD_CAM_PDN2
L22	PAD_PERIPHERAL_EN3
L23	PAD_AUD_DAT_MISO1
L25	PAD_PERIPHERAL_EN1
L26	PAD_AUD_SYNC_MOSI
M22	PAD_PERIPHERAL_EN2
M23	PAD_WATCHDOG
M24	PAD_AUD_CLK_MOSI
M25	PAD_AUD_CLK_MISO
M26	PAD_AUD_SYNC_MISO
N22	PAD_PERIPHERAL_EN6
N23	PAD_PWRAP_SPI0_CSN

N24	PAD_AUD_DAT_MISO0
N25	PAD_SCP_VREQ_VAO
N26	PAD_SCL6
N27	PAD_AUD_DAT_MOSI1
P22	PAD_ANT_SEL0
P23	PAD_PWRAP_SPI0_CK
P24	PAD_PWRAP_SPI0_MI
P25	PAD_PWRAP_SPI0_MO
P26	PAD_SDA6
R22	PAD_ANT_SEL2
R23	PAD_SRCLKENA0
R24	PAD_SRCLKENA1
R25	PAD_PERIPHERAL_EN0
T22	PAD_PERIPHERAL_EN7
T23	PAD_ANT_SEL1
V22	PAD_PERIPHERAL_EN9
W2	PAD_SDA4
W4	PAD_EINT9
W5	PAD_SRCLKENAI
W6	PAD_CAM_PDN0
W22	PAD_SPI1_MI
W23	PAD_DPI_D0
W24	PAD_DPI_D6
W25	PAD_DPI_D2
W26	PAD_DPI_D3
Y2	PAD_SCL4
Y4	PAD_EINT10
Y5	PAD_CAM_PDN1
Y6	PAD_PERIPHERAL_EN11
Y7	PAD_PERIPHERAL_EN10
Y22	PAD_SPI1_MO
Y23	PAD_DPI_HSYNC
Y24	PAD_DPI_D7
Y25	PAD_PERIPHERAL_EN4
Y26	PAD_DPI_D9
AA2	PAD_CAM_RST1
AA3	PAD_CAM_RST0
AA4	PAD_CAM_CLK0
AA5	PAD_KPROW0
AA6	PAD_PWM_A
AA7	PAD_PERIPHERAL_EN14
AA22	PAD_SPI1_CLK
AA23	PAD_DPI_D11

AA24	PAD_DPI_VSYNC
AA25	PAD_DPI_D1
AA26	PAD_DPI_D10
AB1	PAD_SDA2
AB2	PAD_SCL2
AB4	PAD_EINT1
AB5	PAD_EINT0
AB6	PAD_SCL0
AB23	PAD_DPI_D5
AB24	PAD_DPI_D8
AB25	PAD_SPI1_CSB
AB26	PAD_DPI_D4
AC2	PAD_CAM_CLK1
AC3	PAD_EINT4
AC4	PAD_KPCOL0
AC5	PAD_SDA0
AC6	PAD_BPI_BUS0
AC25	PAD_DPI_CK
AC26	PAD_DPI_DE
AD1	PAD_UTXD0
AD2	PAD_URXD0
AD4	PAD_EINT8
AD5	PAD_BPI_BUS5
AD6	PAD_MISC_BSI_CK_3
AD19	PAD_PERIPHERAL_EN13
AD20	PAD_I2S1_MCK
AD21	PAD_I2S1_LRCK
AD22	PAD_PERIPHERAL_EN12
AD25	PAD_PERIPHERAL_EN5
AE1	PAD_EINT7
AE2	PAD_EINT6
AE3	PAD_KPCOL1
AE4	PAD_SCL1
AE5	PAD_BPI_BUS4
AE6	PAD_MISC_BSI_DO_3
AE20	PAD_RFIC0_BSI_D1
AE21	PAD_RFIC0_BSI_EN
AF2	PAD_EINT5
AF3	PAD_SPI_CLK
AF4	PAD_SDA1
AF5	PAD_BPI_BUS3
AF6	PAD_MISC_BSI_CK_2
AF20	PAD_RFIC0_BSI_D0

AF21	PAD_RFIC0_BSI_CK
AF22	PAD_SCL3
AF23	PAD_IDDIG
AF26	PAD_DRVBUS
AG1	PAD_EINT3
AG2	PAD_EINT2
AG3	PAD_SPI_CSB
AG4	PAD_BPI_PA_VM1
AG5	PAD_BPI_BUS2
AG6	PAD_MISC_BSI_DO_2
AG7	PAD_MISC_BSI_DO_1
AG19	PAD_I2S2_DI
AG20	PAD_RFIC0_BSI_D2
AG21	PAD_I2S1_DO
AG22	PAD_SDA3
AG23	PAD_I2S1_BCK
AG24	PAD_INT_SIM1
AG25	PAD_INT_SIM2
AG26	PAD_DSI_TE
AH1	PAD_KPROW1
AH2	PAD_SPI_MO
AH3	PAD_BPI_PA_VM0
AH4	PAD_BPI_OLAT0
AH5	PAD_BPI_BUS1
AH6	PAD_BPI_BUS7
AH7	PAD_MISC_BSI_CK_1
AH22	PAD_BPI_BUS9
AH23	PAD_DISP_PWM
AH24	PAD_SCL5
AH25	PAD_SDA5
AH26	PAD_BPI_OLAT1
AH27	PAD_LCM_RST
AJ2	PAD_SPI_MI
AJ4	PAD_BPI_ANT1
AJ5	PAD_BPI_BUS6
AJ7	PAD_MISC_BSI_CK_0
AJ8	PAD_MISC_BSI_DO_0
AJ22	PAD_BPI_BUS10
AJ24	PAD_BPI_ANT0
AJ25	PAD_BPI_ANT2
AJ26	PAD_BPI_BUS8
AC23	PAD_MSDC1_DAT2
AC24	PAD_MSDC1_CLK

AD23	PAD_SIM2_SCLK
AD24	PAD_MSDC1_DAT0
AD26	PAD_MSDC1_CMD
AD27	PAD_MSDC1_DAT3
AE22	PAD_SIM1_SRST
AE23	PAD_SIM1_SCLK
AE24	PAD_SIM2_SIO
AE25	PAD_SIM2_SRST
AE26	PAD_MSDC1_DAT1
AF24	PAD_SIM1_SIO